



INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	Substitute for form 1449A/PTO		Complete if Known	
	Application Number		10/676,478	
	Filing Date		September 30, 2003	
	First Named Inventor		Adl-Tabatabai, Ali-Reza	
	Group Art Unit		2186	
Examiner Name		Krofcheck, Michael		
Sheet 1 of 1		Attorney Docket No: 42P17411		

US PATENT DOCUMENTS					
Examiner Initial *	Cite No ¹	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear.
		US-6,879,266	04/12/2005	Dye, Thomas A., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"10/676,430 Final Office Action", (4/15/2008), 19 pages	
		"10/676,430 Non-Final Office Action", (11/27/07), 19 pages	
		ABALI, BULENT , et al., "Hardware Compressed Main Memory: Operating System Support and Performance Evaluation", <u>IEEECS Log Number 114250</u> , (2001), 11 pages	
		ABALI, B. , et al., "Memory Expansion Technology (MXT): Software support and performance", <u>International Business Machines Corporation</u> , (2001), 15 pages	
		ABALI, BULENT , et al., "Performance of Hardware Compressed Main Memory", <u>IBM Research Report</u> , IBM T.J. Watson Research Center, Yorktown Heights, NY, (19 July 2000), 13 pages	
		ALAMELDEEN, ALAA R., et al., "Adaptive Cache Compression for High-Performance Processors", <u>Proceedings of the 31st Annual International Symposium on Computer Architecture (ISCA-31)</u> , Munich, Germany, (June 19-23, 2004), 12 pages	
		FRANASZEK, P. A., et al., "Algorithms and data structures for compressed-memory machines", <u>International Business Machines Corporation</u> , (2001), 14 pages	
		HALLNOR, ERIK G., et al., "A Compressed Memory Hierarchy using an Indirect Index Cache", <u>Advanced Computer Architecture Laboratory, EECS Department, University of Michigan, Ann Arbor, MI</u> , (2004), 17 pages	

EXAMINER

DATE CONSIDERED